

**Method and System for Reducing a Dynamic Offset
During the Processing of Asymmetric Signal Sequences**

Claims

1. Method for reducing a dynamic offset during the processing of asymmetric signal sequences, whereby a signal sequence composed of pulses and interpulse periods is subjected to high-pass filtering by means of a high pass containing a capacitor, **characterized in that** in each interpulse period the capacitor (1) is discharged by an amount which depends on the value of the amplitude of the input-side voltage of the high pass.
2. Method according to Claim 1, **characterized in that** the capacitor (1) is partially or completely discharged.
3. Method according to Claim 1 and 2, **characterized in that** the discharge occurs according to a linear or nonlinear characteristic curve.